Giorgos Latmos is a Firmware Engineer at HDL Design House, whose main responsibilities include embedded software development on ASIC and FPGA applications, in terms of supporting next generation SoCs. He works closely with multiple clients including high profile companies also.

Giorgos is a graduate from the department of Electrical Engineering and Computer science at Aristotle University of Thessaloniki. His expertise is on electronics and computer science, while his master dissertation was submitted at Ghent University in Belgium.